

transistor array are spaced from the thin film transistor array by a distance greater than or equal to 1 mm.

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### REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The non-final Office Action of December 17, 2002 has been received and contents carefully reviewed. Applicant gratefully acknowledges that the Examiner has indicated allowance of claims 1-5 and 10-20.

In the Office Action, the Examiner rejected claim 6 under 35 U.S.C. § 102(e) as being anticipated by Komatsu (U.S. Patent No. 6,335,770); rejected claims 7-9 under 35 U.S.C. § 103(a) as being unpatentable over Komatsu in view of Tanaka et al. (U.S. Patent No. 4,295,711).

Applicants respectfully disagree with the rejection of claim 6 under 35 U.S.C. § 102(e). However, to expedite the prosecution of this application, Applicants cancel 7 and amend claim 6 to include the subject matter of claim 7, which would be allowable under 35 U.S.C. § 103(c). Accordingly, claims 1-6 and 8-20 are currently pending in the present application.

Under 35 U.S.C. § 103(c), subject matter developed by another person which qualifies as prior art under 35 U.S.C. § 102(e) shall not preclude patentability under 35 U.S.C. § 103 where the subject matter in the claimed invention were, at the time the invention was made, owned by the same persons or subject to an obligation of assignment to the same person. As set forth in M.P.E.P. § 2141.01, for patent applications filed prior to November 29, 1999, 35 U.S.C. § 103(c) is limited to subject matter developed by another person which qualifies as prior art only under 35 U.S.C. § 102(f) and 35 U.S.C. § 102(g).

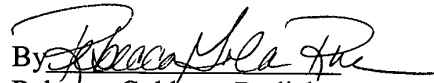
The present application (application serial number 09/892,883) and Komatsu (U.S. Patent No. 6,335,770) were, at the time of the invention of the present application, made and owned by LG. Philips LCD Co., Ltd. Therefore, Applicants respectfully request that the rejection be withdrawn as Komatsu is not valid prior art under 35 U.S.C. § 103(a). Accordingly, Applicant respectfully submits that claims 6, 8 and 9 are in condition for allowance.

In view of the foregoing, each of the presently pending claims in this application is believed to be in immediate condition for allowance. If the Examiner deems that a telephone conference would further the prosecution of this application, the Examiner is invited to call the undersigned attorney at the Washington, D.C. telephone number 202-496-7500. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: March 14, 2003

Respectfully submitted,

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PATENT TRADEMARK OFFICE

**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

6. (Amended) An in-plane switching mode liquid crystal display device comprising:

a plurality of data lines for applying data signals to a thin film transistor array;

a plurality of gate lines for applying gate signals to the thin film transistor array; and

a plurality of common voltage lines for applying a common voltage to the thin film transistor array, wherein the common voltage lines provided in an outer area of the thin film transistor array are spaced from the thin film transistor array by [a predetermined distance.] a distance greater than or equal to 1 mm.